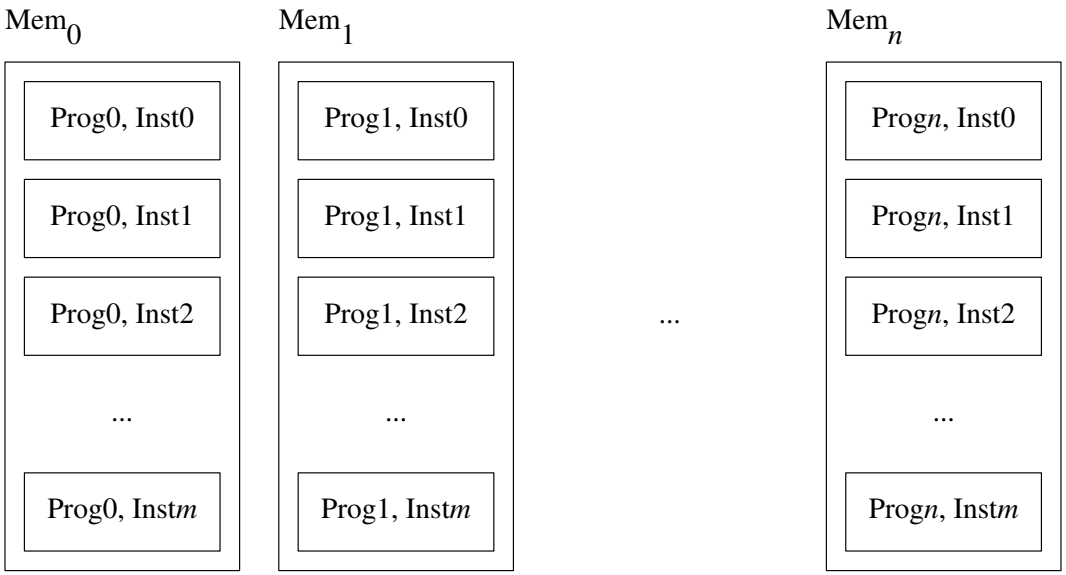

Figure 1: Memory Layout For MIMD Execution