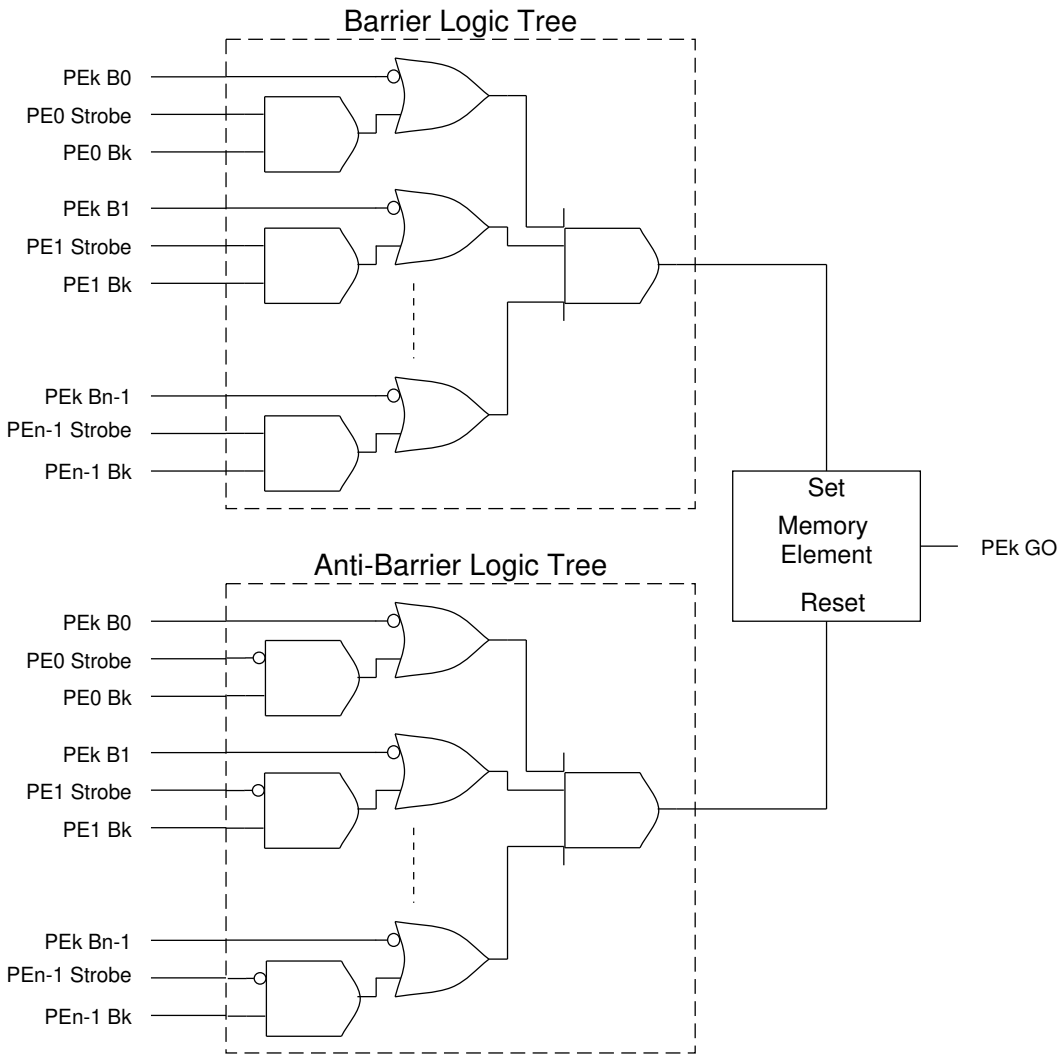
Barrier Logic Tree
PEk B0
PE0 Strobe
PE0 Bk
PEk B1
PE1 Strobe
PE1 Bk
PEk Bn-1
PEn-1 Strobe
PEn-1 Bk
Anti-Barrier Logic Tree
PEk B0
PE0 Strobe
PE0 Bk
PEk B1
PE1 Strobe
PE1 Bk
PEk Bn-1
PEn-1 Strobe
PEn-1 Bk
Set
Memory Element
Reset
PEk GO