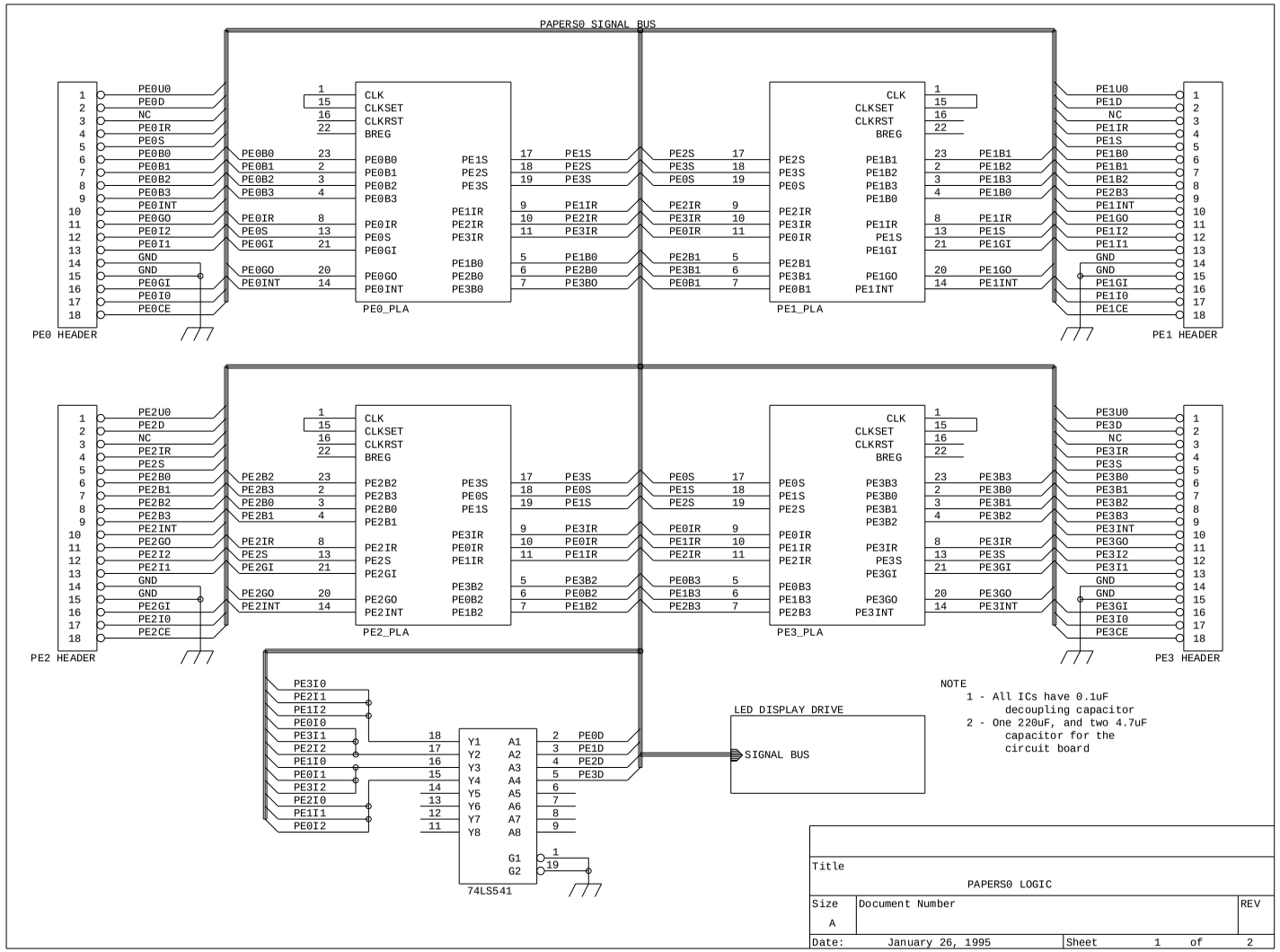
PAPERS0 SIGNAL BUS
PE0 HEADER
PE0_PLA
PE1_PLA
PE1 HEADER
PE2 HEADER
PE2_PLA
PE3_PLA
PE3 HEADER
74LS541
LED DISPLAY DRIVE
SIGNAL BUS
NOTE
1 - All ICs have 0.1uF decoupling capacitor
2 - One 220uF, and two 4.7uF capacitor for the circuit board
Title
PAPERS0 LOGIC
Size
A
Document Number
REV
Date: January 26, 1995
Sheet 1 of 2